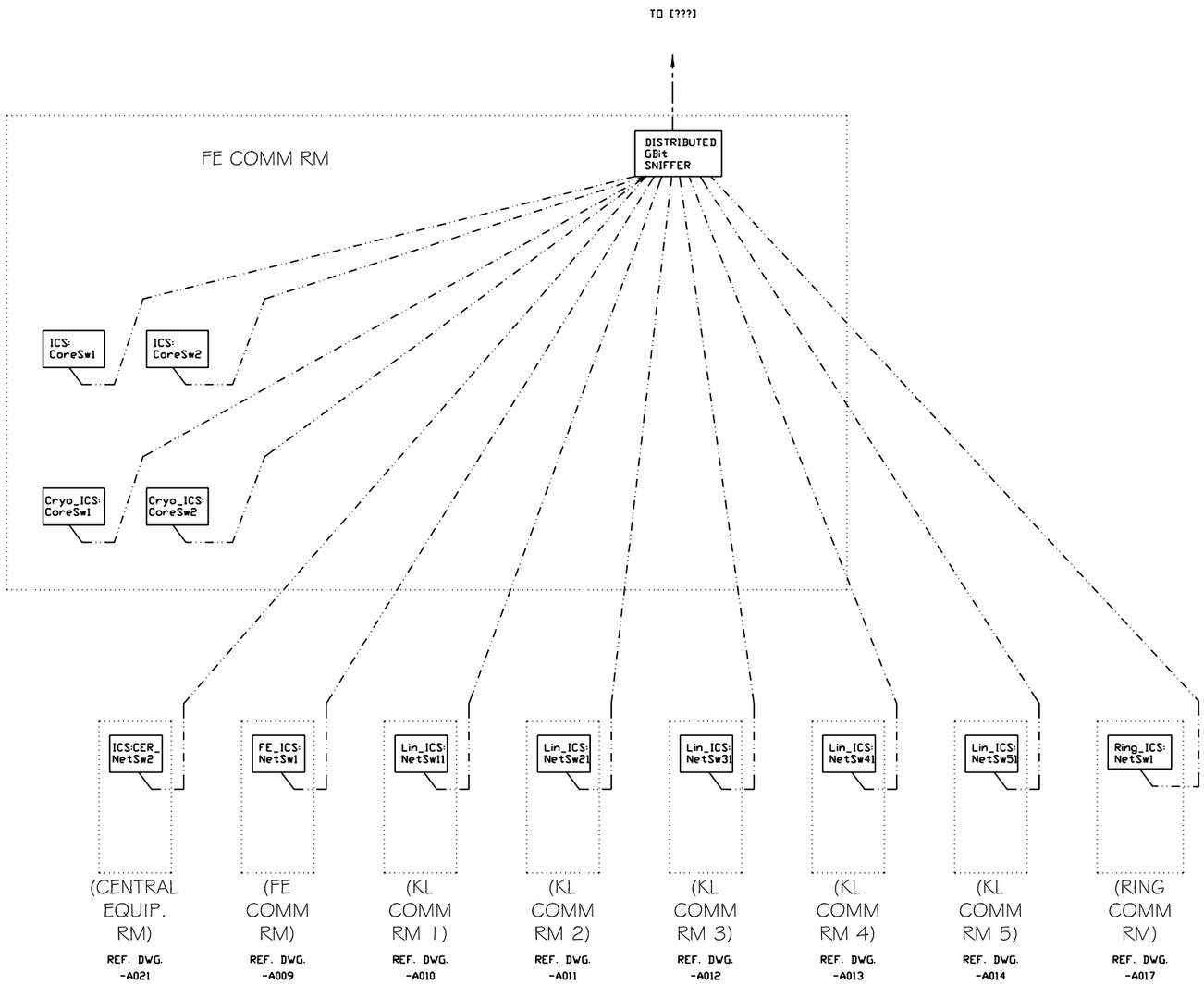
NOTES:

*
WILL BE MOVED FROM FE TEMPORARY CONTROL ROOM TO CLO CENTRAL EQUIPMENT ROOM
ROOM WHEN CLO BUILDING BECOMES AVAILABLE

ICS
DISTRIBUTED GBIT SNIFFER
BLOCK DIAGRAM

KEY:

- RS-232 OR RS-422
- FAST ETHERNET (100BASE-TX)
- FAST ETHERNET (FIBER) (100BASE-FX)
- GIGABIT ETHERNET (1000BASE-SX or -LX)
- FUTURE; NOT INSTALLED UNTIL PHASE "B" OR LATER



07.31.01

REV	DESCRIPTION	BY	CHK	SECT	DEPT	DATE
0	ISSUED FOR APPROVAL	BD	---	---	---	07.11.01
REVISION OR ISSUE PURPOSE		REVISION APPROVALS				

DRAWING APPROVALS		DATE
DES	BILL DEVAN	05.07.01
DWN	BILL DEVAN	05.07.01
CHK		
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LE		
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SNS Spallation Neutron Source

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